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INVENTOR: SHENG-CHI CHIN AND SHY-JAY LIN

A METHOD TO REDUCE PARTICLE LEVEL FOR DRY-ETCH

Enclosed are:

- 5 sheets of drawing(s) - formal.  
 An assignment of the invention to Taiwan Semiconductor Manufacturing Company  
 An associate power of attorney

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Respectfully submitted,  
STEPHEN B. ACKERMAN, REG. NO. 37,761

jc612 U.S. PRO  
09/310256  
05/12/99

A METHOD TO REDUCE PARTICLE LEVEL FOR DRY-ETCH

BACKGROUND OF THE INVENTION

(1) Field of the Invention

The invention relates to the field of semiconductor manufacturing, and more specifically to a method to reduce the dry-etch chamber particle level during the power-down procedure of the dry-etch cleaning process for lithography masks.

(2) Description of the Prior Art

The trend within the semiconductor manufacturing industry to micro-miniaturization and the ability to produce chips with sub-micron features has resulted in a continuous increase of the performance of semiconductor chips while at the same time the cost of semiconductor chips has continued to decrease.

Micro-miniaturization of semiconductor chips has been achieved by significant advances in specific semiconductor fabrication disciplines such as photolithography and dry-etching. Sub-micron features in photo-resist layers have been routinely achieved by the use of more sophisticated exposure cameras, as well as by the use of more sensitive photo-resist materials. The successful transfer of the sub-micron images have

been made possible by the development of dry-etching tools and procedures, from an overlying photo-resist layer, to an underlying material that is used in the fabrication of semiconductors. Single wafer etching can now be performed with the tools and procedures used during Reactive Ion Etching (RIE). This allows for individual etching of each single wafer, with end point detection used for only this single wafer. In this manner wafer to wafer uniformity variations, of the layer being patterned using single layer RIE etching, is not as great a problem as previously encountered during batch RIE etching. Large volumes of wafers can therefore be confidently processed using single wafer RIE procedures, with a decreased risk of under or over-etching due to thickness variations of the material being etched.

During conventional dry-etch, the dry-etch can be applied to a stack of thin layers which can include a photo-resist (for patterning the underlying layer) or an anti-reflective coating (ARC, used for covering the surface of an underlying layer and typically formed over an aluminum layer prior to coating the aluminum layer with photoresist). Such etching, however, results in residues or deposits building up on surfaces inside the plasma treatment chamber. Similar buildup of deposits

occurs in plasma treatment chambers wherein deposition is carried out.

A requirement of dry-etching procedures is the ability to maintain a strong end point detection signal at the completion of the dry-etch cycle when dry-etching from wafer to wafer. In using single wafer RIE tools, the wafer being etched is moved to the etch chamber of the single wafer RIE tool. The etch chamber contains a window for the monitoring of the etching sequence. Laser endpoint detection apparatus or optical diodes monitor, through this window, the chemistry of the reactants and their by-products. At the conclusion of the etching cycle the chemistry of the by-products has changed, the end-point detection process monitors this change. If however the window through which the endpoint monitoring takes place becomes layered with adhering RIE products, the endpoint detection signal will decrease in intensity which can at times result in erroneous end-point signals.

Residual reaction products adhere to surfaces in the plasma treatment chamber when a film is dry-etched in a chlorine-based plasma or fluorine-based dry-etches. These residues can contain metals (or silicates, depending on which type of dry-etch is used), chlorine, or organics or their

compounds. The surfaces that the residues adhere to include upper and lower electrode surfaces, walls of the plasma treatment chamber, clamping surfaces, and any other item that the plasma or its byproducts come in contact with. A build-up of these residues deteriorates the etch performance of the dry-etch. As such, the presence of such residues is undesirable.

Using current one-step cleaning procedures results in a violent transition of the plasma in going from the operating condition of rf-on to rf-off. This results in uncontrolled particle levels within the dry-etch chamber, a condition that is very detrimental to the proper operation and control of the dry-etch process. The present invention addresses this operational aspect of the dry-etch process and teaches a method of significantly reducing the particle count of residues or deposits of residual reaction products in the dry-etch chamber at the end of the dry-etch process.

Fig. 1 shows the current Prior Art one-step cleaning process as applied to a (photolithography) mask.

The Fig. 1a and Fig. 1b conditions indicated below refer to the repetition, that is a first (Fig. 1a) and a second (Fig. 1b) execution of the same process of cleaning the dry-etch chamber

under identical operating conditions of the cleaning operation. A one step mode of the dry-etch chamber cleaning process indicates that the operational conditions during the cleaning process remain constant, most specifically the rf power applied during the cleaning process is not varied.

After the dry-clean process has been applied the first time (Fig. 1a) the dry-etch cleaning sequence is stopped and the conditions inside the dry-etch plasma chamber are allowed to stabilize. The dry-clean operating conditions are then reapplied (Fig. 1b) for a given duration, turned off and, after the conditions in the dry-clean chamber have been stabilized, the dry-clean process is considered completed. The processing conditions for the Fig. 1a and Fig. 1b procedures are identical (since Fig. 1b is, as far as operational conditions is concerned, the same as Fig. 1a) and are as follows:

30 mt/600 w ICP/15 w RIE/30 sccm O<sub>2</sub>/5 min.

These operating conditions indicate that the dry-etch chamber that is being cleaned is of the Inductive Coupled Plasma (ICP) type where an ICP rf power of 600 watts is applied during plasma formation within the chamber, this same power is applied during the dry-etch chamber cleaning procedure while for the

cleaning of the dry-etch chamber a RIE etch is applied to the chamber and its contents using 30 sccm of O<sub>2</sub> for a time of 5 minutes. The parameter mt stands for milliTorr and is the operating pressure in the dry-etch chamber during processing.

Fig. 1a shows the dry-clean chamber 10, a wall polymer or other residual reaction products 12 and the mask 12 that is positioned on a table 16. A rf coil 18 is provided around and on the outside of the chamber 10, the rf coil 18 generates plasma that attacks the wall polymer 14. The wall polymer 14 evaporates as a consequence of the energy provided by the coil 18 thereby making the cleaning the inside of the dry-etch chamber 10 possible. The wall polymer or other residual reaction products 14 are removed via the suction pump 20. The dry-etch process in the example shown is a dry-etch for a Phase Shift Mask (PSM) with MoSiON etchant and O<sub>2</sub> descum. The term descum refers to the process of removing scum whereby scum is an organic or inorganic residue that has collected on the sidewalls of the chamber 10. Using the indicated process, the particle count of the wall polymer or other residual reaction products 12 within the chamber is difficult to control while the particle level is unstable. It is clear that a low particle count is key for the yield of the mask production.

Fig. 1b shows the wall polymer or other residual reaction product molecules 22 being distributed throughout the chamber 10 after the rf coil 18 has been activated at part of the Inductively Coupled Plasma (ICP) process and the RIE etch for the cleaning of the chamber and its contents has been initiated. Fig. 1b represents the power-on, Fig. 1a of the ICP chamber cleaning process.

Fig. 1c shows the condition within the dry-etch chamber between the Fig. 1a and Fig. 1b cycle of the dry-etch process. A number of wall polymer or other residual reaction product modules 24 have settled on the surface of mask 12, a number of other wall polymer or residual reaction product molecules 26 and 28 remain attached to the walls of the dry-etch chamber in a pattern that is not equal on both sides of the chamber but that is determined by complex factors of rf power distribution within the chamber, ionization and polarity of the gas molecules, etc.

Fig. 1d shows the repeat of the dry-etch cleaning process forming Fig. 1b of the dry-etch clean process.

Fig. 1e shows the distribution of the wall polymer or other residual reaction product molecules after Fig. 1b of the dry-etch cleaning process has been completed and after the internal

conditions of the chamber have stabilized. It is clear from this that a significant number of the wall polymer or other residual reaction product molecules have been deposited on the surface of the mask 12, which has a negative effect on mask yield.

Noteworthy also is a comparison between entries Fig. 1c and Fig. 1e. This comparison will show that no significant difference has been gained in the distribution of impurity particles within the dry-etch chamber in going from Fig. 1e to Fig. 1c, this despite the fact that the dry-etch process was executed one additional time between these two entries. The repeat execution of the dry-etch process did not result in an improvement of the impurity count within the dry-etch chamber.

US 5,215,619 (Chen et al.) shows a plasma reactor with a cleaning operation.

US 4,786,392 (Kruchowski et al.) shows a fixture for cleaning a plasma etcher.

SUMMARY OF THE INVENTION

It is the primary objective of the invention to provide a method of reducing particle count within the dry-etch chamber at the end of the dry-etch chamber cleaning process.

Another objective of the present invention is to reduce the need for cleaning of the dry-etch chamber.

Yet another objective of the present invention is to prolong the lifetime of Inductively Coupled Plasma (ICP) or any other type of High Density Plasma (HDP) dry-etch processing kits.

In accordance with the objectives of the invention, the invention teaches a multi-step method of reducing applied power during the process of cleaning the dry-etch chamber. The ICP rf power (that is used to agitate the wall polymer or other residual reaction products within the dry-etch chamber) is reduced between each of these consecutive power-down steps of the cleaning of the dry-etch chamber.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 shows the sequence of events during the original, Prior Art one-step rf power-down mode.

Fig. 2 shows the sequence of events during the multi-step rf power-down mode of the invention.

Fig. 3 shows a graphic depiction of the particle count for both the original Prior Art one step rf power-down mode and the multi-step rf power-down mode of the invention.

Fig. 4 shows a graph further illustrating the results obtained using the power-down procedure of the invention.

DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now specifically to Fig. 2, there is shown the cleaning process of the invention as applied to a (photolithography) mask. Fig. 2a shows the dry-clean chamber 40, a wall polymer or other residual reaction product 42 and the mask 44 that is positioned on a table 46. A rf coil 48 is provided around and on the outside of the chamber 40, the rf coil provides the electrical energy to the polymer 42. The

polymer, as a consequence of the energy provided by the ICP coil 48, evaporates inside the dry-etch chamber 40. Excess of wall polymer or other residual reaction product is removed via the suction pump 50.

Fig. 2b shows the wall polymer or other residual reaction product molecules 52 being distributed throughout the chamber after the rf coil ICP 48 has been activated and the RIE etch has been started.

Fig. 2b represents the first step of the power-down sequence of the dry-etch process. This first step is out of a total of six dry-etch power-down steps within the scope of the invention. Each of the six steps applies gradually decreasing ICP rf power. The term step in this context is to be interpreted as referring to one step in the complete execution of the power-down procedure of the dry-clean process. The complete execution of the power-down procedure contains six steps, each step taking place under specific and unique processing conditions. In going from a preceding step to a following step the rf power applied is gradually reduced while the duration of the last five steps is shorter than the duration of the first step.

It must be emphasized at this point that, although the example illustrated in Fig. 2 is described as a six step cleaning procedure, the invention is not limited to a reduction of applied ICP rf power in six steps. Key to the invention is that the ICP rf power is, during the process of cleaning the ICP dry-etch chamber, reduced in a controlled and gradual manner. The example discussed under Fig. 2 has selected six discrete steps for this reduction in applied ICP rf power. It is however completely feasible within the scope of the invention to select a different number of steps or to reduce the applied ICP rf power not as a step function but in accordance with some other one-to-one relationship between the applied ICP rf power and the elapsed time during the cleaning process. This functional relationship could for instance be a linear decrease of the applied ICP rf power during the cleaning of the dry-etch chamber or, as another example, partially linear and partially following another relationship between the applied ICP rf power and the elapsed time such as exponential, parabolic, partial sinusoidal, etc.

Fig. 2c shows the condition within the dry-etch chamber during execution of dry-etch power-down step 2 within the sequence of six steps of the present invention. No wall polymer or other residual reaction product molecules have settled on the

surface of mask 44 and no wall polymer or other residual reaction product molecules have returned to the original source of the wall polymer or other residual reaction product.

Fig. 2d shows the subsequent steps 4, 5 and 6 of the dry-etch power-down sequence of the present invention. No wall polymer or other residual reaction product molecules have settled on the surface of mask 44 and no wall polymer or other residual reaction product molecules remain attached to the original source of these molecules, excess wall polymer or other residual reaction product molecules have continuously been removed from the chamber by means of the suction pump 50.

Fig. 2e shows the final condition within the dry-etch chamber after applying the six step power-down sequence of the present invention. Due to the gradual reduction of the ICP rf power combined with the continued removal of wall polymer or other residual reaction product from the chamber, the chamber has been effectively emptied of all depositions. As a consequence no impurity deposition will take place on the surface of mask 44 thereby eliminating the negative effect on yield that such impurities had on Prior Art mask production.

The processing conditions for the six step dry-etch cleaning process of the invention are indicated below. It must be emphasized that the indicated six different steps are executed in the sequence indicated while there is no time lag in going from one step to the following step.

Step 1: 30 mt/600 w ICP/15 w RIE/30 sccm O<sub>2</sub>/2.5 min

Step 2: 30 mt/560 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec

Step 3: 30 mt/520 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec

Step 4: 30 mt/480 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec

Step 5: 30 mt/440 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec

Step 6: 30 mt/400 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec.

It must be noted that steps 2 through 6 the RIE time required is considerably shorter than the RIE time required for step 1. Particle count could be reduced within the dry-etch chamber by repeating the above indicated step 1 a number of times, the number of times being determined by the required reduction in the particle count. Under the present invention the particle count can be significantly reduced, as shown by measurement results obtained and presented below, whereby this reduction can be obtained without incurring a penalty of time required to achieve this objective.

Fig. 3 shows a graphic depiction of the particle count for both the original Prior Art one step rf power-down mode and the multi-step rf power-down mode of the invention.

Fig. 3a shows the Prior Art particle count as a function of time. Repetitive executions of the Prior Art power-down procedure will result in gradually reducing the particle count as shown by the gradual decrease of the height of the columns that represent the particle count. This gradual reduction will however require an extended period of time to accomplish the objective of particle reduction. It is also not clear that the particle count can be reduced to zero following this approach.

Fig. 3a, column 10 shows the particle count after the application of the Prior Art cleaning procedure with a rf power level of 600 Watts. In going from column 10 to column 20 to column 30 (and possibly additional columns, not shown), the dry-etch chamber cleaning process is halted and the dry-etch chamber is allowed to establish a stable molecular condition internal to the chamber. Fig. 3a, column 20 shows the particle count after the application of the Prior Art cleaning procedure with a rf power level of 560 Watts. Fig. 3a, column 30 shows the particle count after the application of the Prior Art cleaning procedure with a rf power level of 520 Watts.

Fig. 3b shows the particle count obtained under the present invention. The graph shows the particle count as a function of time for the various levels of applied ICP rf power. Fig. 3b, level 10 indicates the EA count achieved during the first step of the dry-etch chamber power-down sequence of the invention, level 20 indicates the EA count achieved during the second step of the dry-etch chamber power-down sequence of the invention, the EA level for the remaining steps are indicated by levels 30 through 60 whereby level 60 is the last step of the power-down sequence of the invention. It is clear that the reduction of particles as a consequence of the invention is significant.

It is noteworthy to observe, by comparing Fig. 3a with Fig. 3b, that the events presented in Fig. 3a represent a multiplicity of events whereby, in between each of the events, the dry-etch chamber cleaning operation was halted and the conditions within the dry-etch chamber were allowed to stabilize. The EA count presented in Fig. 3b represents one continuous process whereby the conditions within the dry-etch chamber were not interrupted and were therefore not allowed to reach a stable condition in going from one step to the following step in the cleaning procedure.

Fig. 4 further highlights the reduction in particles achieved by applying the power-down sequence of the invention. The Y-axis represents the particle count, the X-axis represents different times or dates on which the particle counts were measured.

The graph in Fig. 4 represents two different and distinct time sequences within the period during which particle measurements were taken.

The period of time indicated with Fig. 4a represents a series of measurements taken over a period of several months. These measurements represent the Prior Art method of power-down for the dry-etch process, this power-down being one fixed step of processing conditions, specifically relating to the rf power as applied to the ICP coil and the time over which the power-down procedure was performed. The measured particle count over this period is between 170 and 225 EA.

Fig. 4b represents the particle count measured also over a period of several months but in this case using the power-down procedure of the invention. It is clear in comparing the latter test results with the test results obtained using the Prior Art power-down procedure that the power-down procedure of the

invention results in significant reduction of the defect count. The measured particle count over the period where the dry-etch power-down procedure of the invention was used is between 24 and 37 EA.

The intuitive and perhaps somewhat simplified explanation for the different results obtained in reducing the particle count within the dry-etch chamber using the two different approaches is as follows.

Using the Prior Art procedure, particles within the dry-etch chamber are, after the rf power is removed, allowed to remain within the chamber and to settle in a position or location that is determined by uncontrolled and uncontrollable conditions of environment within the chamber. These positions are, as far as removing the particles from the chamber is concerned, completely arbitrary and therefore not necessarily conducive to obtaining optimum results of complete removal of the particles from the chamber.

In using the power-down procedure of the invention, the particles within the chamber remain in an agitated state and can therefore not settle down in arbitrary locations or positions. During the power-down procedure of the invention the sucking

action of the pump continues to remove particles. By slowly reducing the ICP rf power while not, as under the Prior Art, completely shutting the rf power down, the pumping action can continue to actively and effectively remove the particles and can, in this manner, obtain superior results of removing the particles from the dry-etch chamber.

Although the invention has been described and illustrated with reference to specific illustrative embodiments thereof, it is not intended that the invention be limited to those illustrative embodiments. Those skilled in the art will recognize that variations and modifications can be made without departing from the spirit of the invention. It is therefore intended to include within the invention all such variations and modifications which fall within the scope of the appended claims and equivalents thereof.

What is claimed is:

1. Providing a method of reducing dry-etch cleaning chamber particle count at the end of power-down for said dry-etch chamber, comprising:

providing a dry-etch cleaning chamber;

positioning a workpiece within said cleaning chamber; and

following a dry-etch chamber power-down procedure whereby said power-down is applied in a controlled and gradual manner.

2. The method of claim 1 wherein said dry-etch chamber is of the Inductive Coupled Plasma variety said dry-etch chamber having a holding member with a surface which holds wafers or masks to be etched and an enclosing member which encloses the holding member to form a chamber for the plasma whereby plasma agitation occurs by an rf coil arrangement surrounding said enclosing member whereby said rf coil arrangement produces a large voltage change near the enclosing member thereby enabling the cleaning of the enclosing member by the plasma itself whereby furthermore plasma gasses can continuously be removed from said enclosing member by means of a suction pump arrangement attached to said enclosing member.

3. The method of claim 1 wherein said dry-etch cleaning chamber provides plasma gasses within the dry-etch cleaning chamber said providing to be followed by applying a RIE etch.

4. The method of claim 1 wherein said workpiece is a photolithography mask.

5. The method of claim 1 wherein said workpiece is the surface of a semiconductor substrate.

6. The method of claim 1 wherein said workpiece is any surface within the construction of a semiconductor device to which a dry-etch operation must be performed.

7. The method of claim 1 wherein said following a dry-etch chamber power-down procedure is a power-down procedure whereby the rf power supplied to the ICP coil is gradually reduced in a sequence of six steps each of said six steps to be executed as part of a particular sequence and without time interruption each step immediately following the preceding step in numerical sequence whereby the time during which said RIE is applied varies and is adjusted in accordance with the step within the sequence wherein said steps are identified as step 1 through step 6.

8. The method of claim 7 wherein the processing conditions for said step 1 are specified as 30 mt/600 w ICP/15 w RIE/30 sccm O<sub>2</sub>/2.5 min.

9. The method of claim 7 wherein the processing conditions for said step 2 are specified as 30 mt/560 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec.

10. The method of claim 7 wherein the processing conditions for said step 3 are specified as 30 mt/520 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec.

11. The method of claim 7 wherein the processing conditions for said step 4 are specified as 30 mt/480 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec.

12. The method of claim 7 wherein the processing conditions for said step 5 are specified as 30 mt/440 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec.

13. The method of claim 7 wherein the processing conditions for said step 6 are specified as 30 mt/400 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec.

14. The method of claim 7 wherein said six step power down procedure is modified to a sequence of N steps wherein N is a whole integer number other than zero and where the processing conditions for each of the consecutive steps are specified as 30 mt/AA w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec wherein said AA w ICP represents a value of applied power for the consecutive steps within said sequence said applied power to decrease concurrent with increases in the value of N and whereby said applied power varies from an initial high value to a final low value in incremental numbers whereby said incremental numbers may or may not be multiples of AA/N and whereby furthermore said initial high and final low values are experimentally determined and optimized for each dry-etch chamber power down procedure.

15. The method of claim 1 wherein said following a dry-etch chamber power-down procedure is a power-down procedure whereby the rf power supplied to the ICP coil is reduced in a sequential and controlled manner during the elapsed time of the cleaning process of said dry-etch chamber whereby at all times during said elapsed time there is a one-to-one relationship between the rf power supplied to the ICP coil and the elapsed time of the cleaning cycle said relationship being defined by a specific mathematical equation where said equation may be different for different dry-cleaning cycles.

16. Providing a method of reducing particle count at the end of Power-down for a Inductive Coupled Plasma (ICP) dry-etch cleaning chamber, comprising:

providing a ICP dry-etch cleaning chamber;

positioning a workpiece within said cleaning chamber; and

following a dry-etch chamber power-down procedure whereby said power-down is a six step power-down procedure whereby said six steps of said power-down procedure follow in a given numerical and fixed sequence and without interruption or time-lag in between any of said six steps and whereby step 1 is specified as

30 mt/600 w ICP/15 w RIE/30 sccm O<sub>2</sub>/2.5 min. whereby further

step 2 is specified as 30 mt/560 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30

sec. whereby further step 3 is specified as 30 mt/520 w ICP/15 w

RIE/30 sccm O<sub>2</sub>/30 sec. whereby further step 4 is specified as 30

mt/480 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec. whereby further step 5

is specified as 30 mt/440 w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 whereby

further step 6 is specified as 30 mt/400 w ICP/15 w RIE/30 sccm

O<sub>2</sub>/30 sec.

17. The method of claim 16 wherein said six step power down procedure is modified to a sequence of N steps wherein N is a whole integer number other than zero and where the processing conditions for each of the consecutive steps are specified as 30 mt/AA w ICP/15 w RIE/30 sccm O<sub>2</sub>/30 sec wherein said AA w ICP

represents a value of applied power for the consecutive steps within said sequence said applied power to decrease concurrent with increases in the value of N and whereby said applied power varies from an initial high value to a final low value in incremental numbers whereby said incremental numbers may or may not be multiples of AA/N and whereby furthermore said initial high and final low values are experimentally determined and optimized for each dry-etch chamber power down procedure.

18. The method of claim 16 wherein said following a dry-etch chamber power-down procedure is a power-down procedure whereby the rf power supplied to the ICP coil is reduced in a sequential and controlled manner during the elapsed time of the cleaning process of said dry-etch chamber whereby at all times during said elapsed time there is a one-to-one relationship between the rf power supplied to the ICP coil and the elapsed time of the cleaning cycle said relationship being defined by a specific mathematical equation where said equation may be different for different dry-cleaning cycles.

19. The method of claim 16 wherein said dry-etch chamber having a holding member with a surface which holds wafers or masks to be etched and an enclosing member which encloses the holding member to form a chamber for the plasma whereby plasma

agitation occurs by an rf coil arrangement surrounding said enclosing member whereby said rf coil arrangement produces a large voltage change near the enclosing member thereby enabling the cleaning of the enclosing member by the plasma itself whereby furthermore plasma gasses can continuously be removed from said enclosing member by means of a suction pump arrangement attached to said enclosing member.

20. The method of claim 16 wherein said workpiece is a photolithography mask.

21. The method of claim 16 wherein said workpiece is the surface of a semiconductor substrate.

22. The method of claim 16 wherein said workpiece is any surface within the construction of a semiconductor device to which a dry-etch operation must be performed.

ABSTRACT

The invention teaches a multi-step method for shutting down the dry-etch process. The ICP rf power is reduced between each of these consecutive power-down steps of the dry-etch process, the complete power-down sequence consists of six steps. These six steps are executed in sequence and without interruption and form the totality of the dry-etch chamber power-down procedure.

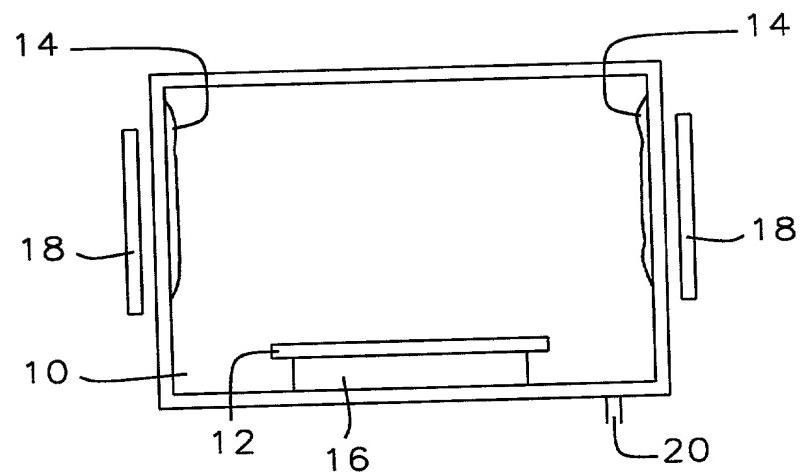


FIG. 1a

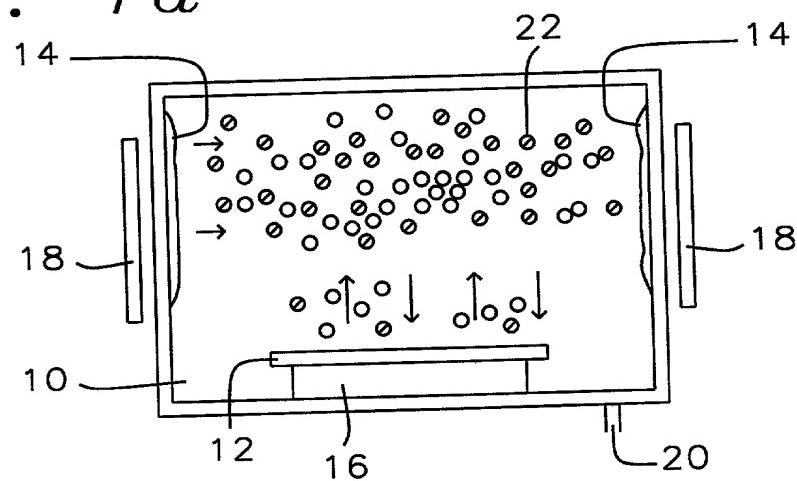


FIG. 1b

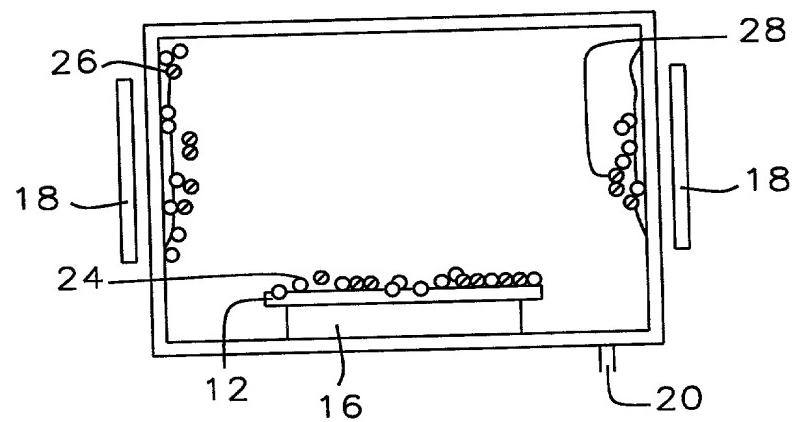


FIG. 1c

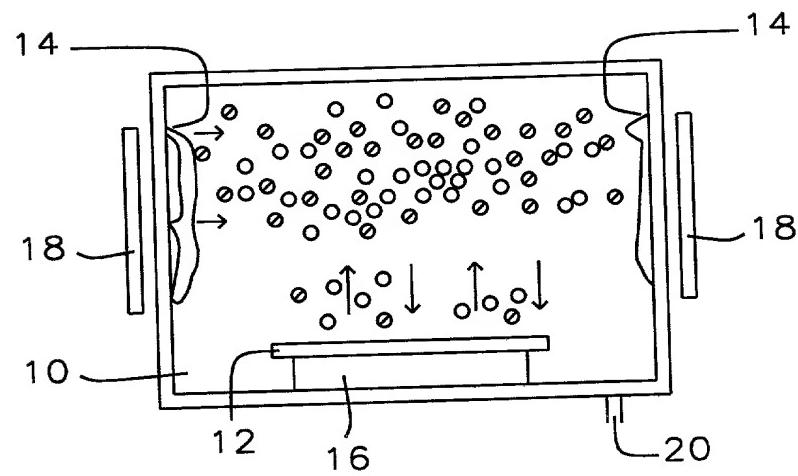


FIG. 1d

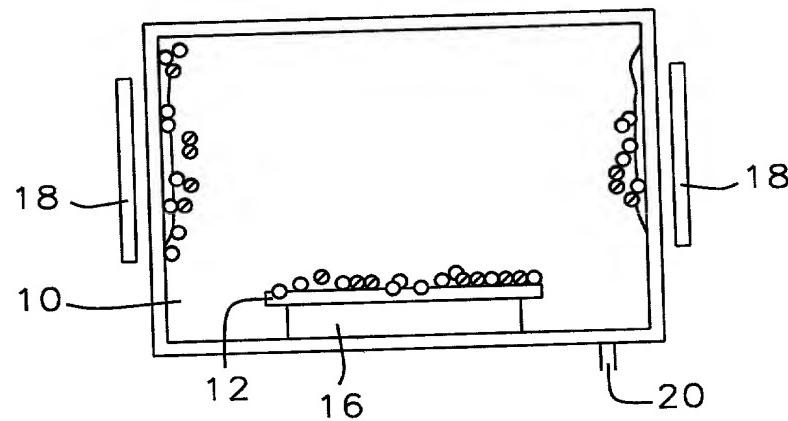


FIG. 1e

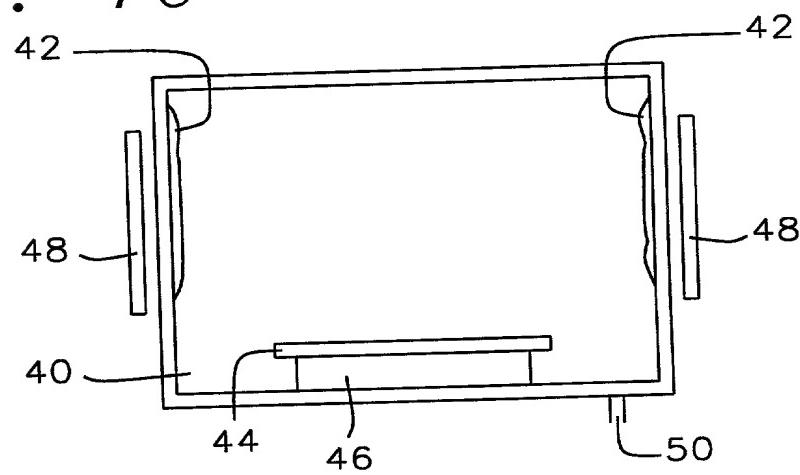


FIG. 2a

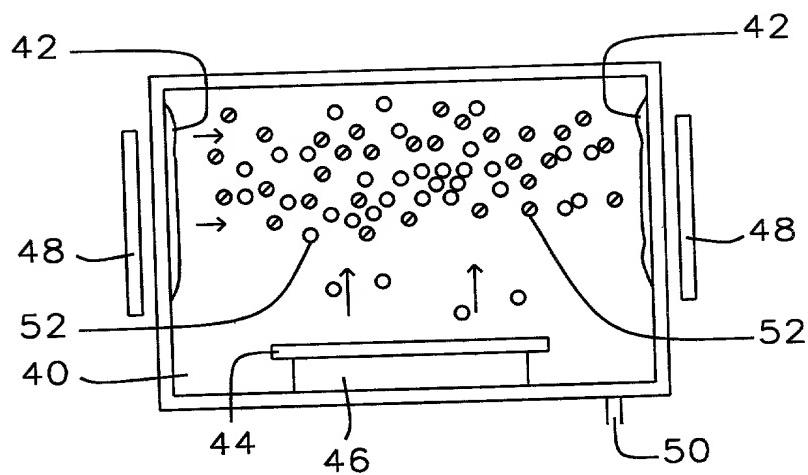


FIG. 2b

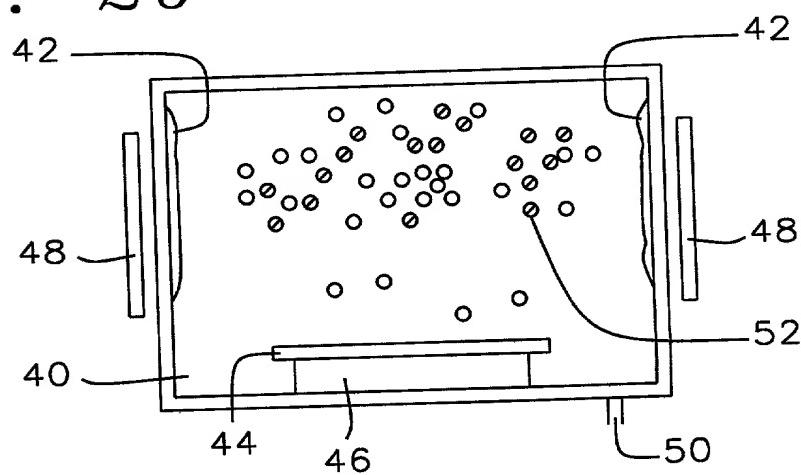


FIG. 2c

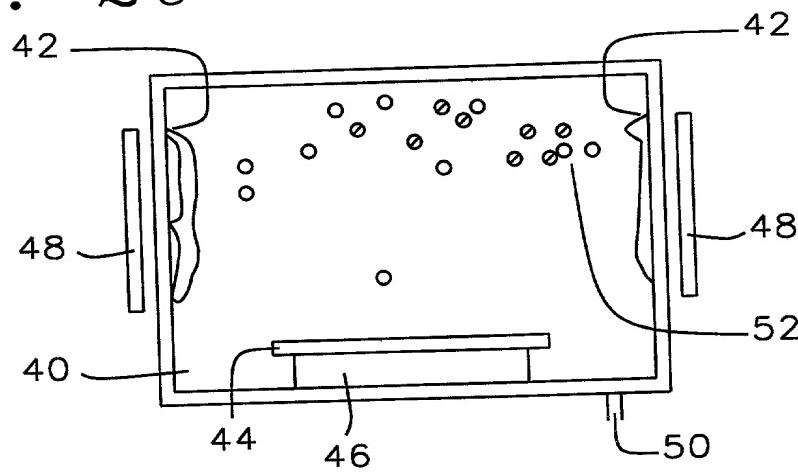


FIG. 2d

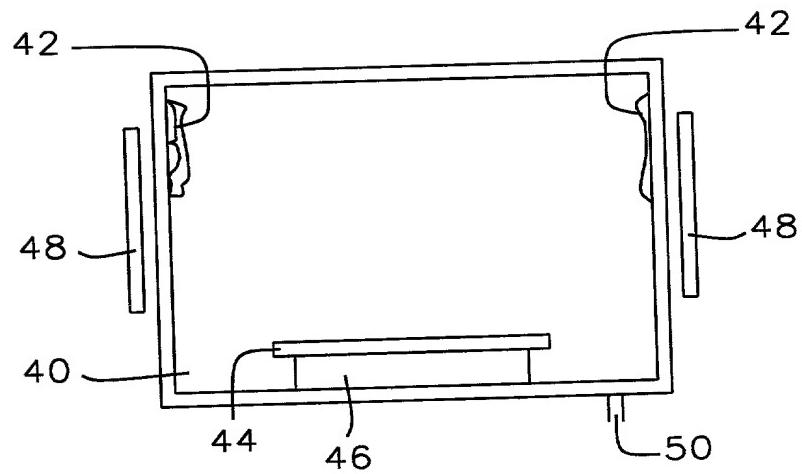


FIG. 2e

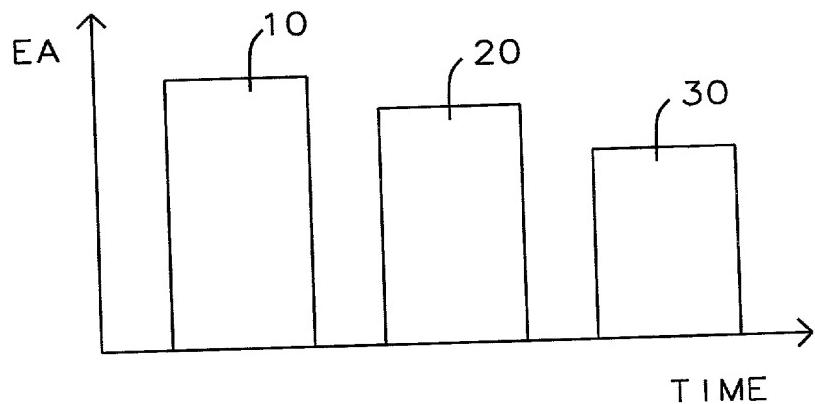


FIG. 3a

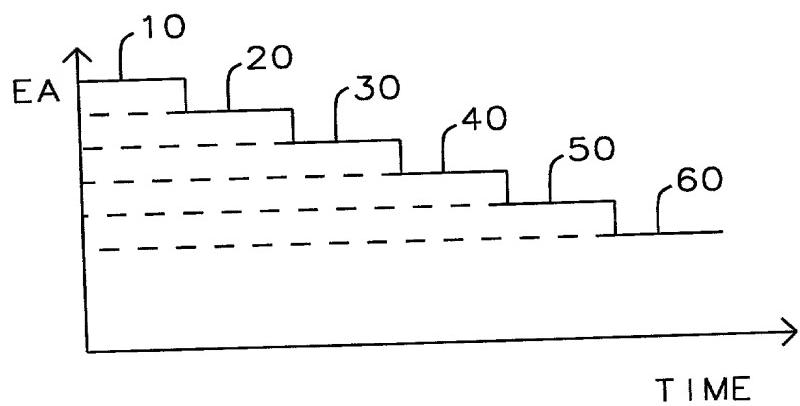


FIG. 3b

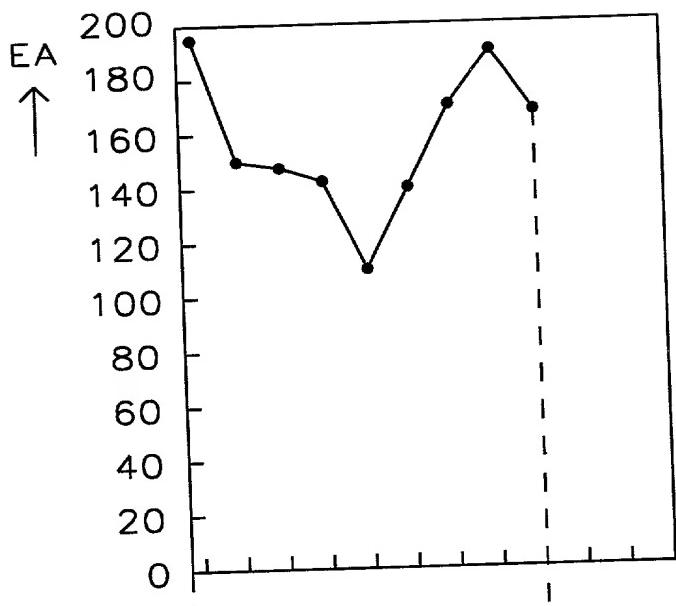


FIG. 4a

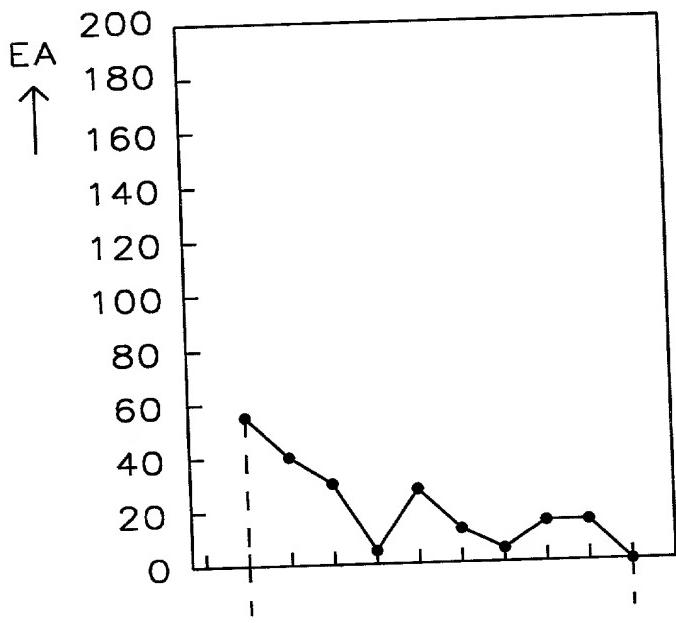


FIG. 4b

# DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

DOCKET NO. TS98-574

As a below named Inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled  
**A Method To Reduce Particle Level For Dry-Etch**

the specification of which (check one)

X is attached hereto.

was filed on \_\_\_\_\_

Application Serial No. \_\_\_\_\_

and was amended on \_\_\_\_\_ (if applicable)

I hereby state that I have reviewed and understand the contents of the above Identified specification including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed:

(Number)	(Country)	(Day/Month/Year Filed)
(Number)	(Country)	(Day/Month/Year Filed)

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.) (Filing Date) (Status) (patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name & registration no.)

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Full name of fifth inventor

Date

Inventor's signature

Residence

Citizenship

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Residence

Citizenship

Post Office Address